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GP/2812

PATENT

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March 13, 2002

Date

Heather Piaskowski

Heather Piaskowski

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Sion C. Quinlan and Tim J. Bales

Attorney Docket No.: 500986.02

Serial No. : 10/057,205

Group Art Unit : 2812

Filed : January 25, 2002

Title : SEMICONDUCTOR PACKAGE ASSEMBLY AND METHOD FOR ELECTRICALLY  
ISOLATING MODULES

TECHNICAL STAFF 8800

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner of Patents  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references, as required under 37 C.F.R. § 1.98, are enclosed). Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Andrew F. Pratt

Registration No. 48,985

AFP:pwr

Enclosures:

Postcard

Form PTO-1449

Cited References (12)

1420 Fifth Avenue, Suite 3400

Seattle, WA 98101

Tele: (206) 903-8800

Facsimile: (206) 903-8820

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FORM PTO-144  
(REV 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

500986.02

APPLICATION NO.

10/057,205

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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APPLICANT(S)

Sion C. QUINLAN and Tim J. BALES

FILING DATE

January 25, 2002

GROUP ART UNIT

~~2812~~ 2829

MAR 25 2002

PATENT AND TRADEMARK OFFICE

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	CLASSIFICATION NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/	AA				TECHNICAL	

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
/	AB					YES NO

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

SBG	AC	Apple Computer, Inc., "Firewire Technology Fact Sheet," obtained at website "http://a772.g.akamai.net/7/772/51/f7f756ae8e5bf0/www.apple.com/firewire/pdf/FireWireFS-b.pdf", March 13, 2002, pp. 1-4				
SBG	AD	McMunn, Lee James, "The Physical Layer," obtained at website "http://www.awstevenson.demon.co.uk/SYSNOTES/physic.htm," March 12, 2002, pp. 1-2				
SBG	AE	Willis, P. J., "Communication Protocols," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/chapter2_6.html," August 17, 2001, p. 1				
SBG	AF	Willis, P. J., "The OSI Model," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/sections2_6_1.html," August 17, 2001, p.1				
SBG	AG	Willis, P. J., "Physical Layer," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/subsection2_6_1_1.html," August 17, 2001, p. 1				
SBG	AH	Willis, P. J., "Data Link Layer," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/subsection2_6_1_2.html," August 17, 2001, p.1				
SBG	AI	Willis, P. J., "Network Layer," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/subsection2_6_1_3.html," August 17, 2001, p.1				
SBG	AJ	Willis, P. J., "The Physical Layer," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/section2_7_1.html," August 17, 2001, pp. 1-2				
SBG	AK	Willis, P. J., "The Datalink Layer," obtained at website "http://www.maths.bath.ac.uk/~pjw/NOTES/networks/section2_7_2.html," August 17, 2001, pp. 1-2				
SBG	AL	Embedded Systems Programming, "Fundamentals of Firewire," obtained at website "http://www.embedded.com/1999.9906.9906feat2.htm," August 28, 2001, pp. 1-14				

AB. Gz

8/21/03

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 500986.02		APPLICATION NO. 10/057,205	
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>				APPLICANT(S) Sion C. QUINLAN and Tim J. BALES			
				FILING DATE January 25, 2002		GROUP ART UNIT 2812-2829	
SBG	AM	Microprocessor and Microcomputer Standards Committee of the IEEE Computer Society, "P1394a Draft Standard for a High Performance Serial Bus (Supplement)," The Institute of Electrical and Electronics Engineers, Inc., June 30, 1999, pp. T-27-43-67 <i>SBG 8/21/03</i>					
SBG	AN	Lucent Technologies, Inc., "IEEE 1394 Isolation," Application Note, November 1998, obtained at website "http://www.agere.com/1394/docs/AP98074-01.pdf," pp. 1-16					
EXAMINER <i>At B. G.</i>				DATE CONSIDERED 8.21.03			
* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant(s).							

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